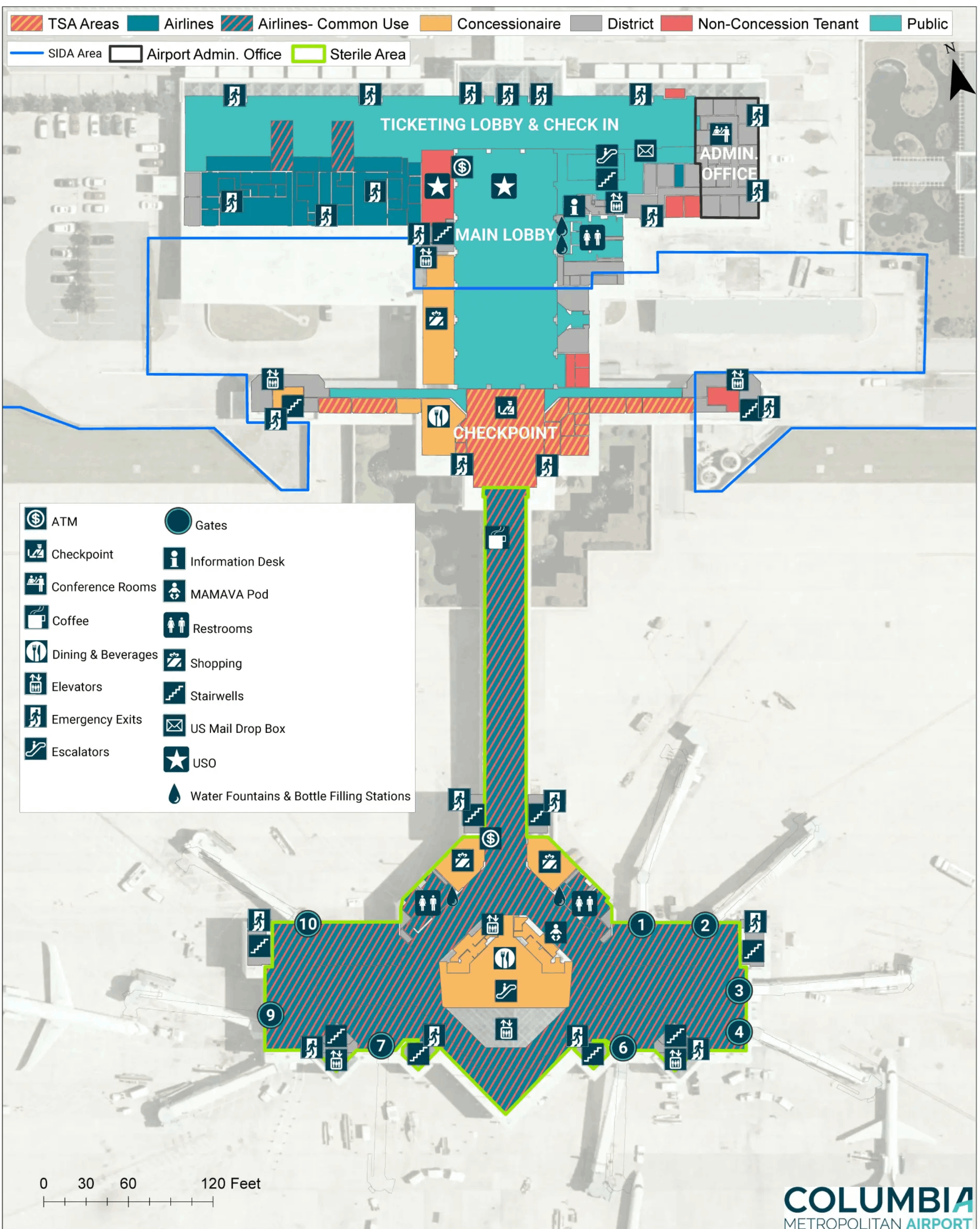


CAE TERMINAL UPPER LEVEL



CAE TERMINAL LOWER LEVEL

